



2923  
JFW

IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

**PATENT APPLICATION**

Robert Alan Hamm  
Rose Fasano Kopf  
Robert William Ryan  
Alaric Tate  
Yu-Chi Wang

**CASE** 12-14-9-7-5

**Serial No.** 09/433204 **Group Art Unit** 2823

**Filed** November 4, 1999

**Examiner** W. Coleman

**Title** Method Of Fabricating A Heterojunction Bipolar Transistor

**COMMISSIONER FOR PATENTS**

P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

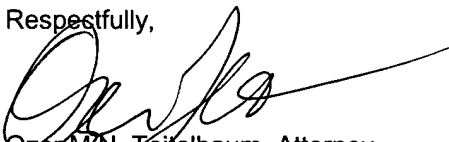
**SIR:**

Enclosed is an amendment in the above-identified application.

**NO ADDITIONAL FEE REQUIRED**

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 12-2325** as required to correct the error.

Respectfully,



Ozer M.N. Teitelbaum, Attorney  
Reg. No. 36698  
973-386-8803.

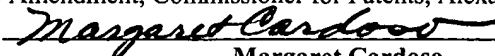
**Date:** May 14, 2004

**Docket Administrator (Room 3J-219)**  
Lucent Technologies Inc.  
101 Crawfords Corner Road  
Holmdel, NJ 07733-3030

PT 16 (10/01)

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: MS Amendment, Commissioner for Patents, Alexandria, VA 22313-1450 on **May 14, 2004**.



Margaret Cardoso



Serial No. 09/433,204

**IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE**

**Patent Application**

<b>Inventor:</b>	Hamm et al.	<b>Case:</b>	12-14-9-7-5
<b>Serial No.:</b>	09/433,204	<b>Group Art Unit:</b>	2245
<b>Filing Date:</b>	November 4, 1999	<b>Examiner:</b>	W. Coleman
<b>Title:</b>	<b>A METHOD OF FABRICATING A HETEROJUNCTION BIPOLAR TRANSISTOR</b>		

**THE COMMISSIONER OF PATENTS AND TRADEMARKS  
WASHINGTON, D.C. 20231**

**RESPONSE UNDER 37 C.F.R. § 1.111**

In response to the Examiner Dang's Office Action dated February 19, 2004, please enter the following response as indicated in the following pages.